



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Confirmation No.: 5935

Suguru TACHIBANA et al.

Art Unit: 2838

Serial No.: 10/808,532

Examiner: B. Vu

Filed: March 25, 2004

Docket No.: 100353-00185

For: SEMICONDUCTOR INTEGRATED CIRCUIT USING BAND-GAP
REFERENCE CIRCUIT (As Amended)

AMENDMENT UNDER 37 C.F.R. §1.111

Mail Stop Amendment

October 18, 2005

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

This paper is filed in response to the Office Action mailed June 27, 2005, the period of time for responding having been extended until October 27, 2005, by attached Petition for a One-Month Extension of Time to Respond in the above-captioned patent application. In the prosecution of the above-captioned patent application, please consider the following: